

REVISIONS		
ISSUE	APP	DATE
1	R.J.P.	5-18-86
ON TABLE "A" ADDED OPTS -019 THRU -027. ECR C4661		
2	R.J.P.	12-09-87

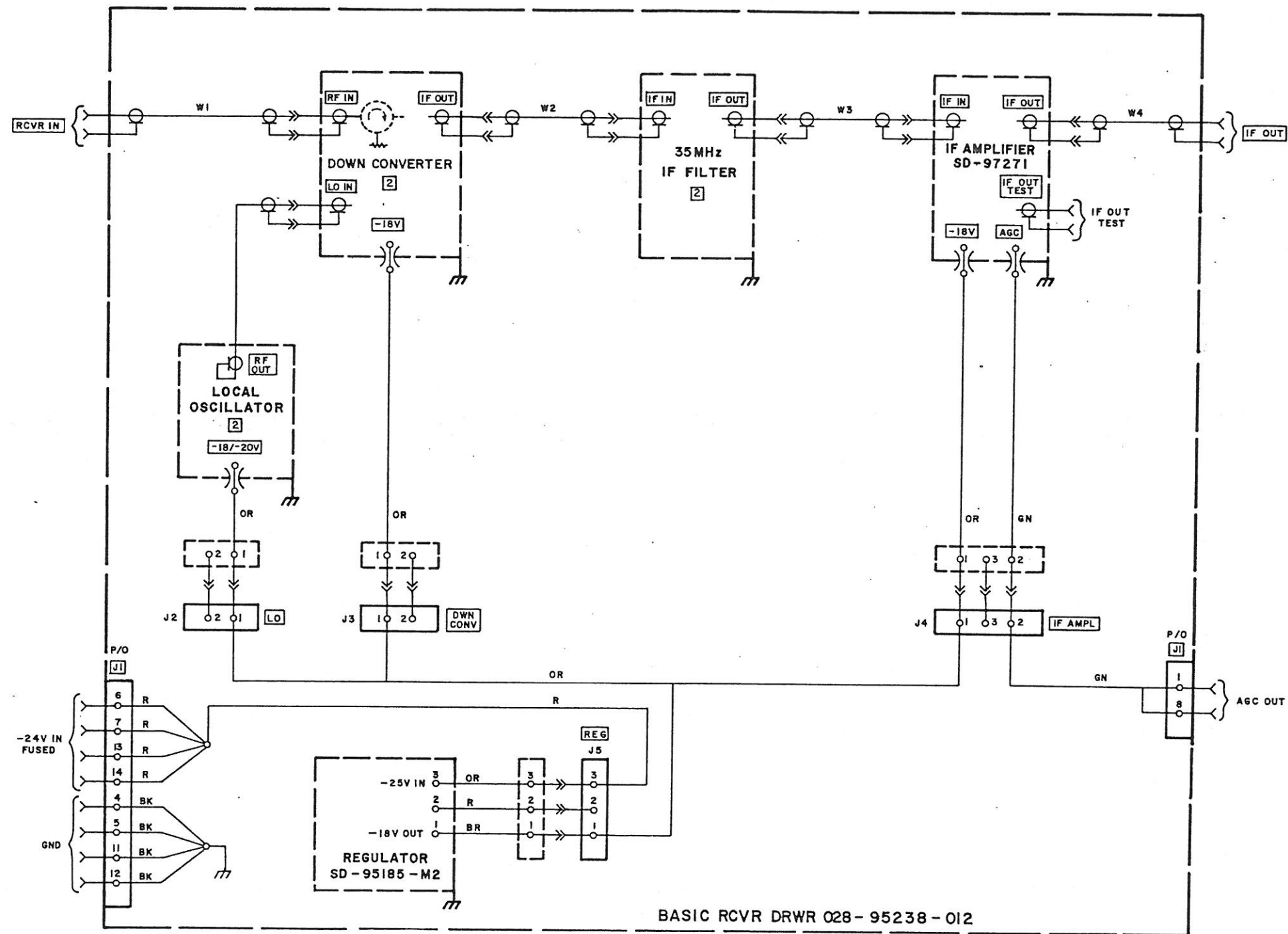
TABLE "A" [2]

OPT	FREQUENCY RANGE (GHz)	SYSTEM CAPACITY (Mbit/s)	IF BANDWIDTH	IF FILTER	DOWN CONVERTER	LOCAL OSCILLATOR	FRONT PANEL DESCRIPTION
-001	1.7 - 1.9	2.048	2.2	SD-97324	SD-97361-001	SD-97369-001	"A" FRAME WITH ORDERWIRE
-002							"A" FRAME WITHOUT ORDERWIRE
-003							"B" FRAME PROTECTED
-004		"A" FRAME WITH ORDERWIRE					
-005		6.312	5.0	SD-97564			"A" FRAME WITHOUT ORDERWIRE
-006		"B" FRAME PROTECTED					
-007		"A" FRAME WITH ORDERWIRE					
-008		8.44	12.0	SD-97838			"A" FRAME WITHOUT ORDERWIRE
-009		"B" FRAME PROTECTED					
-010	1.9 - 2.1	2.048	2.2	SD-97324	SD-97361-002	SD-97833-001	"A" FRAME WITH ORDERWIRE
-011							"A" FRAME WITHOUT ORDERWIRE
-012							"B" FRAME PROTECTED
-013		"A" FRAME WITH ORDERWIRE					
-014		6.312	5.0	SD-97564			"A" FRAME WITHOUT ORDERWIRE
-015		"B" FRAME PROTECTED					
-016		"A" FRAME WITH ORDERWIRE					
-017		8.44	12.0	SD-97838			"A" FRAME WITHOUT ORDERWIRE
-018		"B" FRAME PROTECTED					
-019	2.1 - 2.3	2.048	2.2	SD-97324	SD-97361-003	SD-97438-001	"A" FRAME WITH ORDERWIRE
-020							"A" FRAME WITHOUT ORDERWIRE
-021							"B" FRAME PROTECTED
-022		"A" FRAME WITH ORDERWIRE					
-023		6.312	5.0	SD-97564			"A" FRAME WITHOUT ORDERWIRE
-024		"B" FRAME PROTECTED					
-025		"A" FRAME WITH ORDERWIRE					
-026		8.44	12.0	SD-97838			"A" FRAME WITHOUT ORDERWIRE
-027		"B" FRAME PROTECTED					

NOTES:
 1 - UNLESS OTHERWISE SPECIFIED, ALL WIRES ARE #24 AWG.
 2 - OPTIONS EQUIPPED PER TABLE "A".

MATERIAL:			
FINISH:			
PROCESS:			
USED ON: LR4-2000			
SCALE:	ENG. BY: R.J.P.	APP.:	
DATE: 15-08-86	DWN. BY: L.K.	APP.:	
			
RECEIVER			
2 DECIMAL TOLERANCE ± .015"	SHEET 1 OF 2	SIZE	
3 DECIMAL TOLERANCE ± .005"			
SD-96882-M2			D

SD-96882-M2 D		
REVISIONS		
ISSUE	APP.	DATE
1	R.J.P.	5-12-86
ECN C4561		
2	R.J.P.	25-03-87
ECN C4739		
3	E.S.	88-0
ECN C4943		
4	B.C.	88-02-
ECN C4963		
5	B.C.	88-04-14



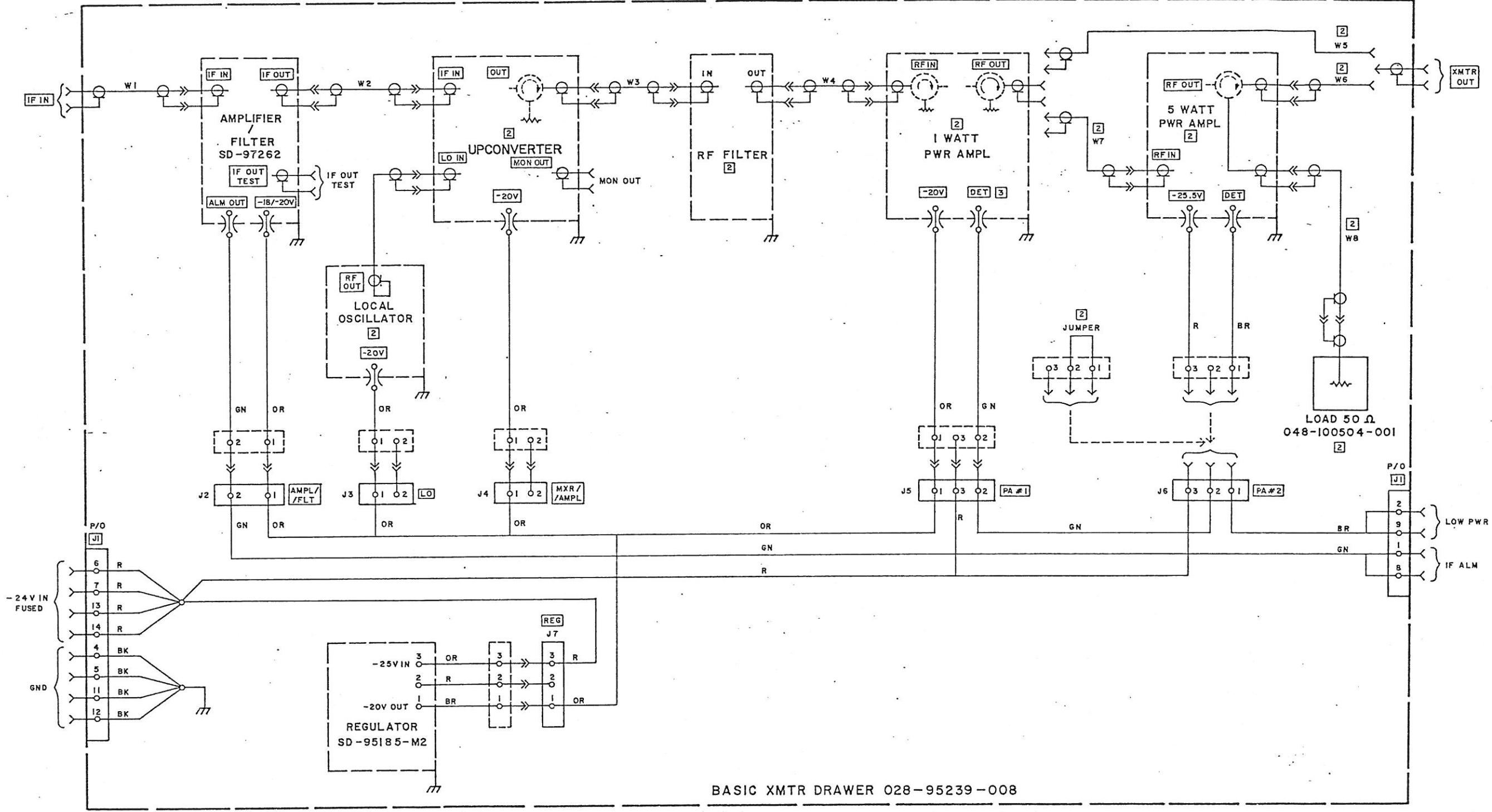
BASIC RCVR DRWR 028-95238-012

LAST NO'S USED:

J5, W4

MATERIAL:		
FINISH:		
PROCESS:		
USED ON: LR4-2000		
SCALE: _____	ENG. BY: R. J. P.	APP.:
DATE: 88-04-11	DWN. BY: L. K.	APP.:
RECEIVER		
2 DECIMAL TOLERANCE ± .015"	SHEET 2 OF 2	SIZE
3 DECIMAL TOLERANCE ± .		
SD-96882-M2		D

REVISION		
ISSUE	APP.	DATE
1	R.J.P.	87-05-15
REVISED NO.		C4614
1A	R.J.P.	87-01-15
ADDED OPTS - 003, 006 ECN C4629		
2	R.J.P.	87-04-27
ON OPTS - 003, -004 LOC. OSC. WERE SD-97833. ON OPTS -005, -006 LOC. OSC. WERE SD-97438. ON OPT -004 5W PWR AMPL WAS SD-97890. ON OPT -006 5W PWR AMPL WAS SD-97966. ECN C4739		
3	P.S.	89-02-02
ON OPT -002, -004, -006 5W PWR AMPL WERE SD-97882. SD-97890-M2, SD-98121 RESP. ECN C5229.		
4	P.R.	89-04-06



BASIC XMTR DRAWER 028-95239-008

- NOTES:
- UNLESS OTHERWISE SPECIFIED:
- ALL WIRES ARE #22 AWG.
 - EQUIPMENT OPTIONS ARE EQUIPPED PER TABLE "A".
 - DETECTOR IS NOT USED WHEN 5 WATT OPTION IS EQUIPPED.

TABLE "A" [2]

OPT	FREQ. RANGE (GHz)	POWER	5W PWR AMPL SD-98427	LOAD 50 Ω 048-100504-001	JUMPER	W5	W6, W7, W8	1 WATT POWER AMPLIFIER	LOCAL OSCILLATOR	UPCONVERTER	RF FILTER
-001	1.7 - 1.9	1W	OMIT	OMIT	EQUIP	EQUIP	OMIT	SD-97796-001	SD-97369-M2	SD-97654-001	SD-94517-004
-002		5W	EQUIP	EQUIP	OMIT	OMIT	EQUIP				
-003	1.9 - 2.1	1W	OMIT	OMIT	EQUIP	EQUIP	OMIT	SD-97796-002	SD-97833-M2	SD-97654-002	SD-94517-005
-005		5W	EQUIP	EQUIP	OMIT	OMIT	EQUIP				
-006	2.1 - 2.3	1W	OMIT	OMIT	EQUIP	EQUIP	OMIT	SD-97964	SD-97438-M2	SD-97654-003	SD-94517-006
-004		5W	EQUIP	EQUIP	OMIT	OMIT	EQUIP				

MATERIAL:

FINISH:

PROCESS:

USED ON: LR 4-2000

SCALE: _____ ENG. BY: R.J.P. APP.: _____

DATE: 87-12-15 DWN. BY: L.K. APP.: _____

HARRIS
FARINON CANADA

TRANSMITTER

2 DECIMAL TOLERANCE ±.015"
3 DECIMAL TOLERANCE ±.010"

SHEET 1 OF 1 SIZE

SD-96881-M2 D

REVISIONS		
ISSUE	APP.	DATE
1	E.J.P.	13-8-85
2	R.J.P.	27-6-86
3	E.G.	88-06-20
4	E.G.	89-08-23
5	R.G.	90-05-14

ON OPT -001 FL1 & FL2 WERE 081-102652-001 (ERROR) ECN C4236
 ON TABLE 'A' ADDED OPTS -005 THRU -012, FREQUENCY RANGE AND FIG. NOS. CONFIGURATIONS WERE PER OPTS -001 THRU -004, NOW CHANGED TO FIG 1 THRU FIG 4 RESP. CIRCULATORS WERE STND; NOW PER TABLE 'A'. ON OPT -004 CIRC #3 & #4 WERE 094-18935-101. ECN C4479

ADDED OPTS -013 THRU -015, FIG. 5 & SHEET 2. ECN C5053
 REARRANGED FIG. 4 CONFIG. ECN C5334
 ADDED OPTS -016 THRU -018 AND FIG. 6 ECN C5401

FIG. 1

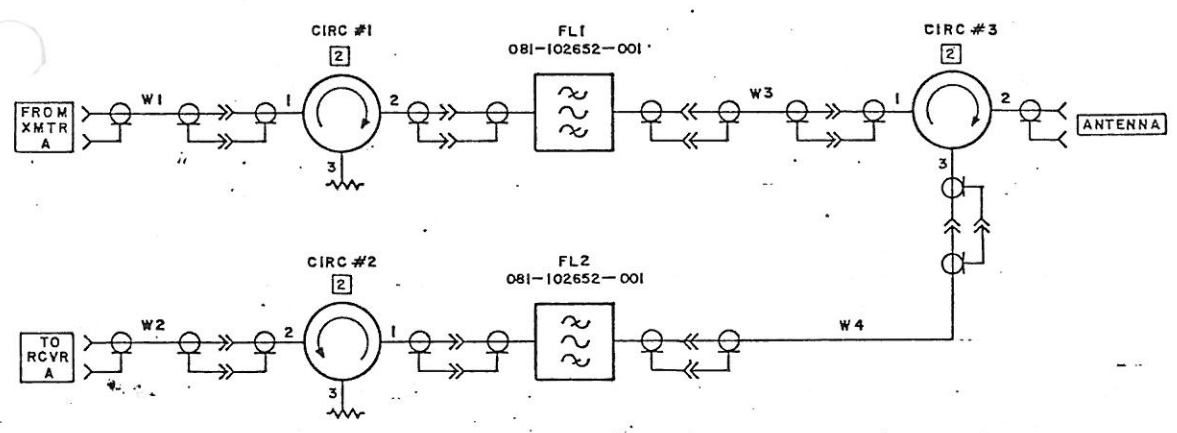


FIG. 2

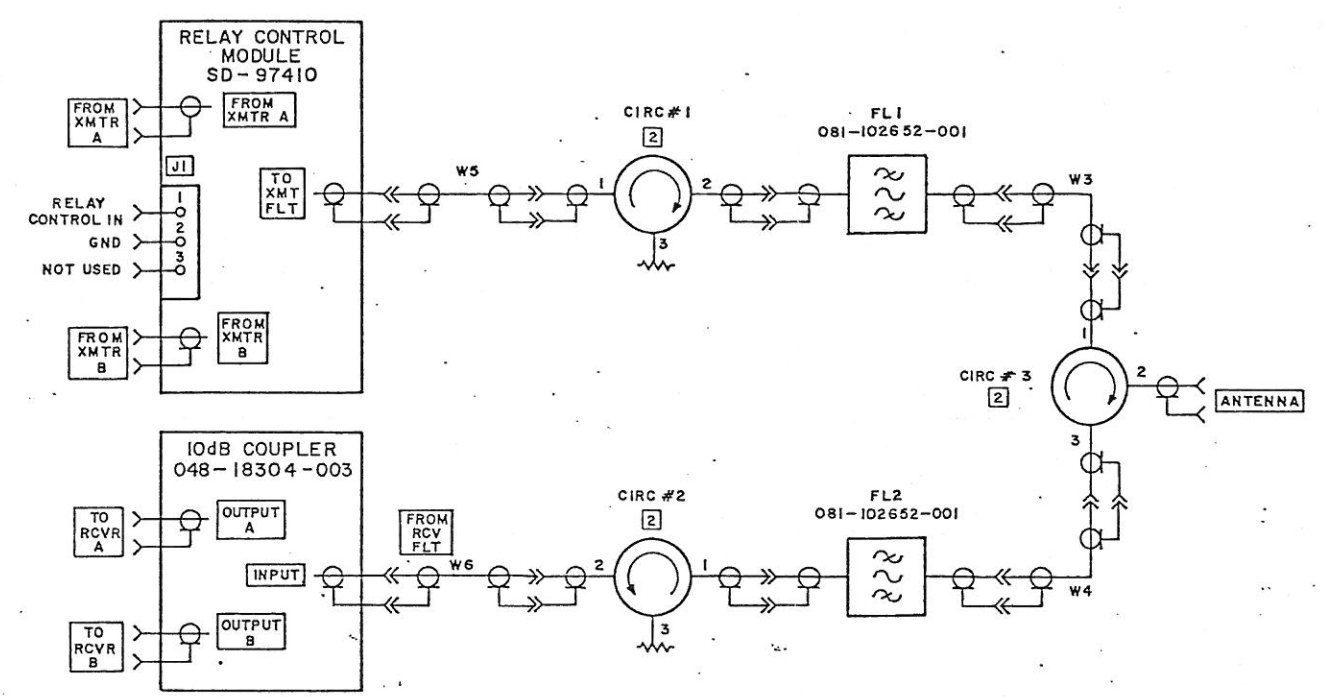


FIG. 4

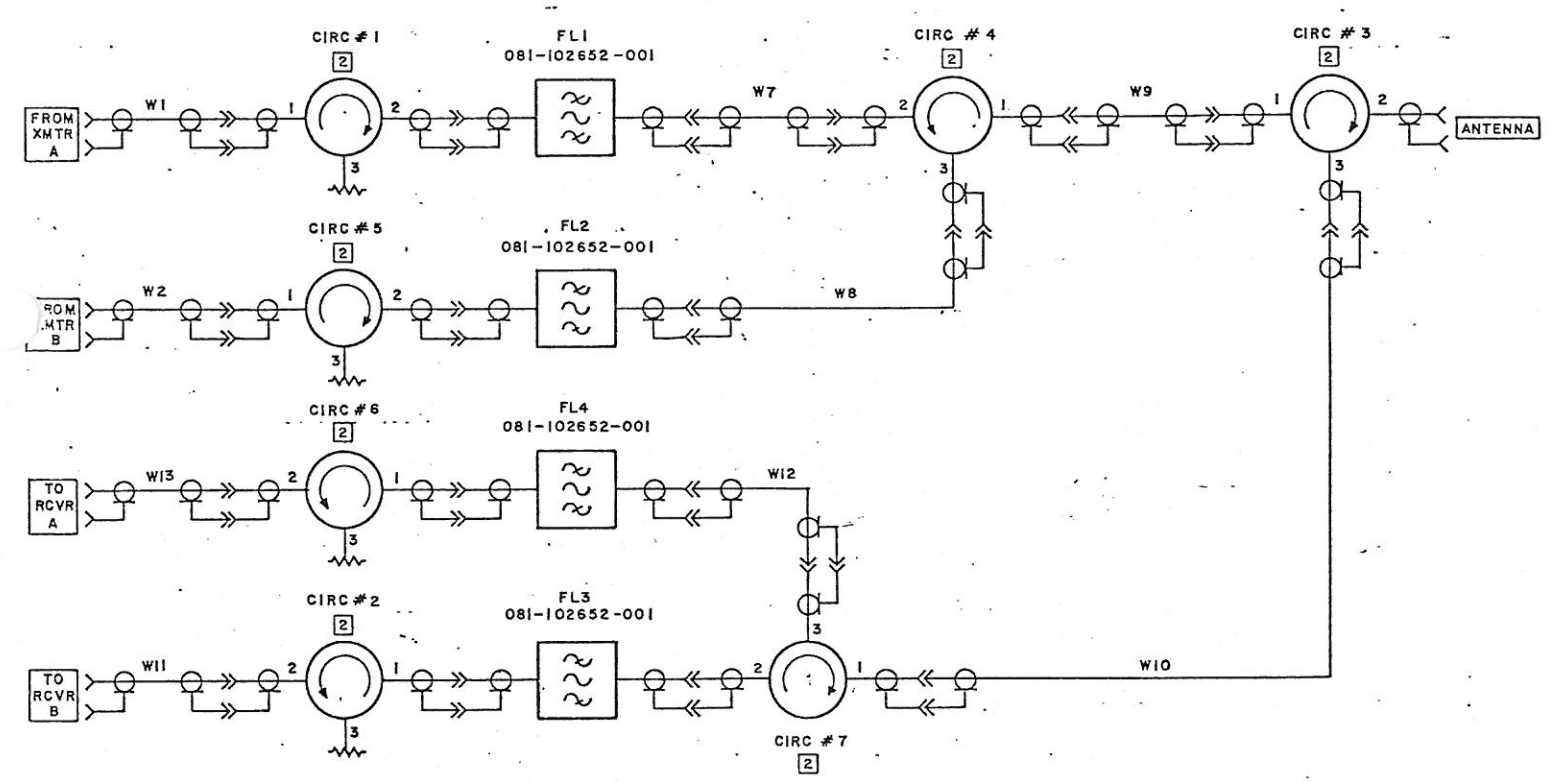
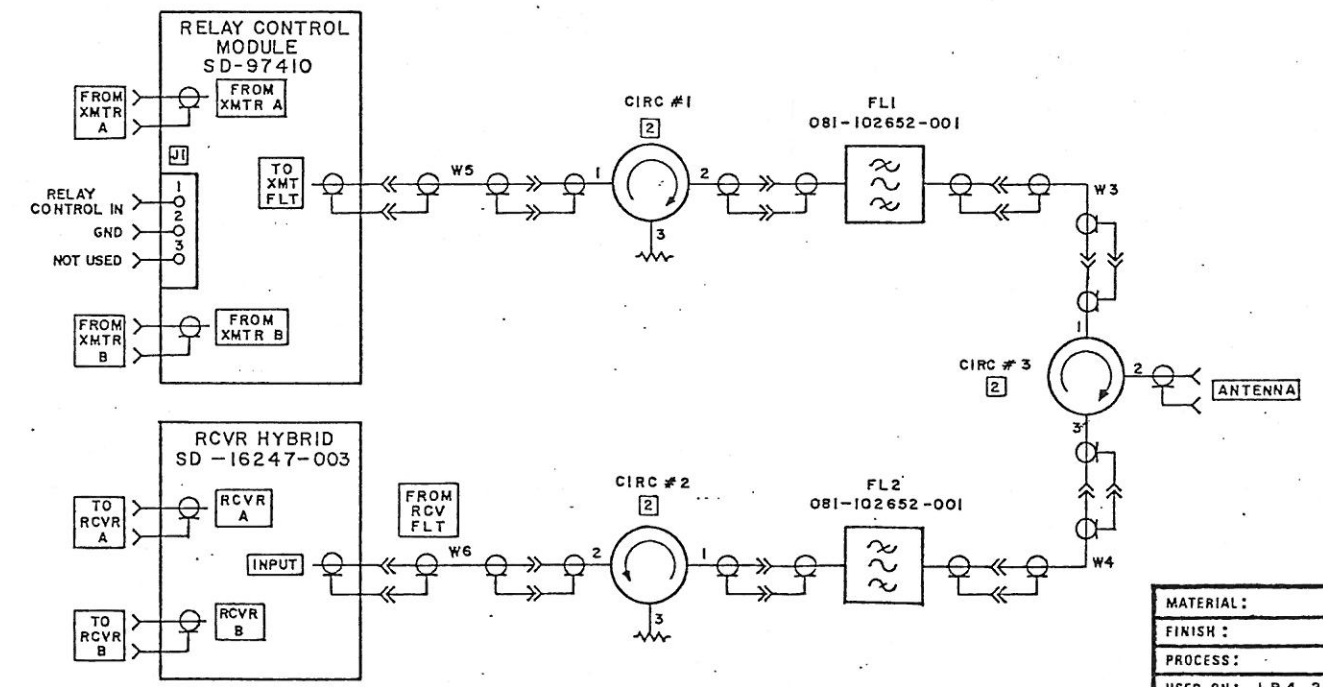


FIG. 3



MATERIAL:

FINISH:

PROCESS:

USED ON: LR 4-2000

SCALE: _____ ENG. BY: R.J.P. APP: _____

DATE: 21/06/85 OWN. BY: L.K. APP: _____

HARRIS
FARINON CANADA

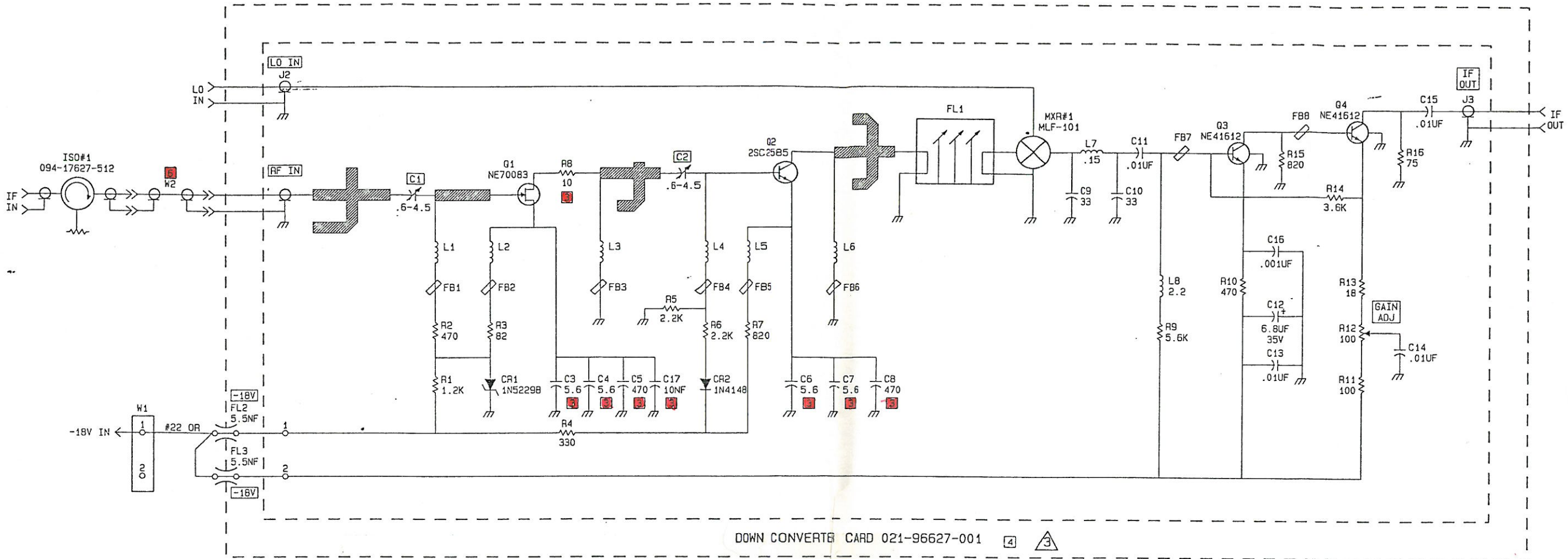
ANTENNA COUPLING UNIT
2 GHz

2 DECIMAL TOLERANCE ±.015"
3 DECIMAL TOLERANCE ±.005"

SHEET 1 OF 2 SIZE

SD-96878 D

REVISIONS		
ISSUE	APP.	DATE
1	B.C.	24-05-85
MXR#1 WAS FAM-42. PCB WAS ISS: 1. ECN C4470		
2	B.C.	18-06-86
ADDED "CAUTION ESS" ECN C4617		
2A	R.J.P.	24-02-87
ADDED W2, TABLE "B" & NOTE 6. ECN C4926		
3	B.C.	88-02-17
RELOCATED FB1-FB6 WERE ON OPPOSITE SIDE OF L1-L6. ADDED C17. ECN C5246.		
4	B.C.	89-02-23
PCB WAS ISS: 2. ECN C5256		
4A	B.C.	89-02-24
W1, PIN 1 WIRE WAS #20 OR. ECN C5324		
4B	R.S.	89-11-04

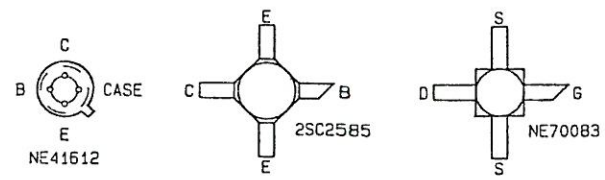


DOWN CONVERTER CARD 021-96627-001

TECHNICAL SUMMARY

Input Characteristics	
Receive Signal (RF)	2300 MHz to 2700 MHz
RF Level	-40 dBm, maximum -94 dBm, minimum
Local Oscillator Signal (L.O.)	2265 MHz to 2735 MHz
L.O. Level	+7 dBm, nominal (-2, +3 dB)
Impedance (RF & L.O.)	50 Ohms
Return Loss	17 dB, minimum
Output Characteristics	
Intermediate Frequency (IF)	35 MHz
IF Level	+3 dBm, maximum (at 1 dB compression point)
Conversion Gain	44 dB, minimum
Gain Flatness	0.3 dB (34 MHz to 36 MHz)
Noise Figure	4 dB, maximum
Image Rejection	20 dB, minimum
3rd-Order Intermodulation Distortion	>40 dB (with IF level = -5 dBm)
Impedance	75 Ohms, unbalanced
Return Loss	24 dB, minimum
Power Requirements	80 mA maximum, at -18 Vdc
Connectors	SMA, female

- NOTES :
- UNLESS OTHERWISE SPECIFIED :
- RESISTOR VALUES ARE IN OHMS ± 5%, 1/4W
- CAPACITOR VALUES ARE IN PICOFARADS
- INDUCTOR VALUES ARE IN MICROHENRIES
- ALL 1% RESISTORS ARE 1/8W.
 - COMPONENT TYPE NUMBERS AND VALUES ARE THOSE NORMALLY PROVIDED. SUBSTITUTIONS OF EQUIVALENT TYPES OR DIFFERING VALUES THAT DO NOT DEGRADE PERFORMANCE MAY BE MADE AT THE TIME OF MANUFACTURE.
 - CHIP RESISTOR & CHIP CAPACITOR.
 - PC BOARD CONFORMAL COATING EQUIPPED PER TABLE "A".
 - TRANSISTOR BASE CONFIGURATION (BOTTOM VIEW) SHOWN BELOW:



6 - EQUIPMENT OPTIONS ARE EQUIPPED PER TABLE "B".

TABLE "A" [4]

OPT	CONFORMAL COATING
A	OMIT
B	EQUIP

TABLE "B" [6]

OPT	DESCRIPTION	W2
-001	LR2-2500 LR4-2500	OMIT
-002	LAD-2500	EQUIP

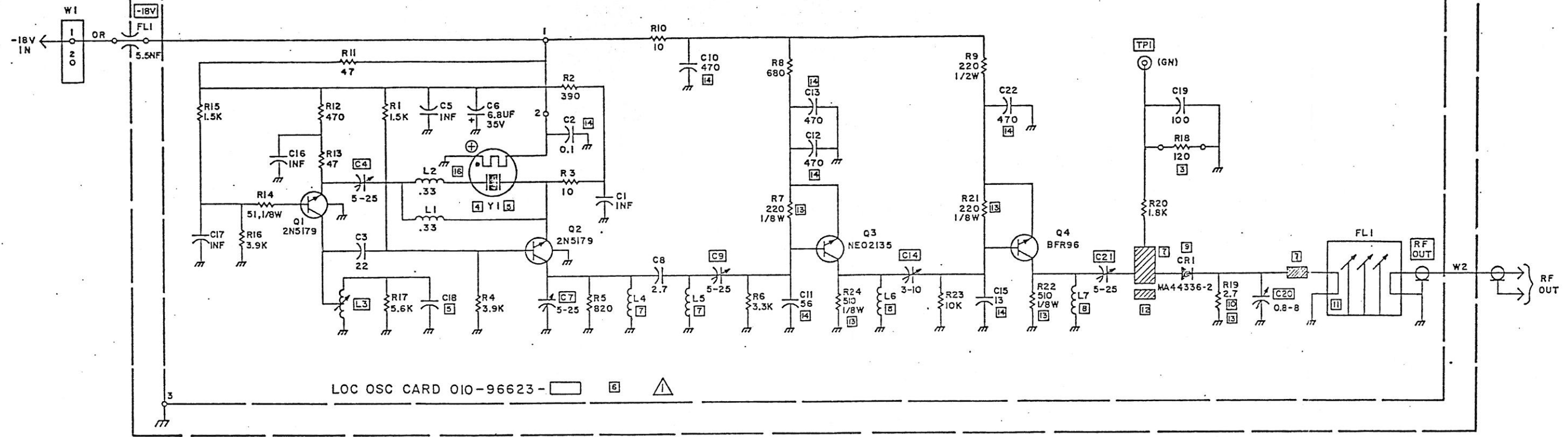
CAUTION ESS
UNIT CONTAINS
ELECTROSTATIC-SENSITIVE
ITEMS. MAINTAIN
ANTISTATIC PROTECTION

MATERIAL :	
FINISH :	
PROCESS :	
USED ON :	[6]
SCALE :	ENG. BY : B.C. APP. :
DATE : 89-11-03	OWN. BY : L.K. APP. :



DOWN CONVERTER

ISSUE NUMBER CROSS-REFERENCE											
2 DECIMAL TOLERANCE ±.015"						SHEET 1 OF 1			SIZE		
3 DECIMAL TOLERANCE ±											
SD	1	2	2A	3	4	4A	4B				
TI	1	1	1A	1A	1A	1A					



LOC OSC CARD 010-96623 - [] [] []

- NOTES:
- UNLESS OTHERWISE SPECIFIED:
 - RESISTOR VALUES ARE IN OHMS ± 5%, 1/4 W.
 - CAPACITOR VALUES ARE IN PICOFARADS.
 - INDUCTOR VALUES ARE IN MICROHENRIES.
 - ALL 1% RESISTORS ARE 1/8 W.
 - COMPONENT TYPE NUMBERS AND VALUES ARE THOSE NORMALLY PROVIDED. SUBSTITUTIONS OF EQUIVALENT TYPES OR DIFFERING VALUES THAT DO NOT DEGRADE PERFORMANCE MAY BE MADE AT THE TIME OF MANUFACTURE.
 - FACTORY SELECTED COMPONENT. NOMINAL VALUE: R18, 120 Ω.
 - CRYSTAL FREQUENCY DETERMINED BY TABLE "A".
 - EQUIPMENT OPTIONS ARE EQUIPPED PER TABLE "B".
 - PC BOARD CONFORMAL COATING EQUIPPED PER TABLE "C".
 - PART OF PC BOARD.
 - L6,7 MAY BE TUNED (EXPANDED OR CONTRACTED) TO GIVE BEST PERFORMANCE.
 - USE ONLY DIODE TYPE MA44336-2 FOR CR1. MOUNT 1/16" ABOVE BOARD SURFACE. SUBSTITUTION NOT ALLOWED. TYPE OF CRYSTAL USED FOR OPTS. -001, -003 IS 086-R134 80° WITH CRYSTAL OVEN 086-20116-020; OPTS. -002, -004 IS 086-R002 WITHOUT CRYSTAL OVEN. TUNING RANGE: 2265 TO 2735 MHz.
 - R19 SHOULD BE MOUNTED APPROX. 3/16" ABOVE BOARD.
 - COUPLING LOOP IS A #22 AWG ENAMEL WIRE MOUNTED APPROX 3/16" ABOVE BOARD.
 - FACTORY TUNED. AN ADDITIONAL STRIPLINE SECTION MAY BE STRAPPED INTO CIRCUIT TO PROVIDE MATCHING FOR CR1.
 - CARBON COMPOSITION ONLY.
 - CHIP CAPACITOR.
 - TRANSISTOR BASE CONFIGURATION (BOTTOM VIEW) SHOWN

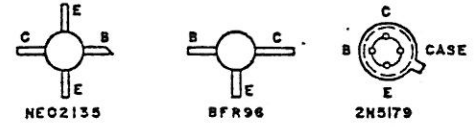


TABLE "A" []

LOC OSC FREQ (MHz)	XTAL FREQ RANGE (MHz)	XTAL FREQ (MHz)
2265 - 2500	94.37-104.17	$F_c \pm \frac{35}{24}$
2500 - 2735	89.28 - 97.68	$F_c \pm \frac{35}{28}$

TABLE "B" []

OPT	FREQ RANGE (MHz)	FREQ STABILITY	OVEN	C18 (PF)
-001	2265 - 2500	.00025%	EQUIP	3.3
-002		.002%	OMIT	
-003	2500 - 2735	.00025%	EQUIP	6.8
-004		.002%	OMIT	

TABLE "C" []

OPT	CONFORMAL COATING
A	OMIT
B	EQUIP

TECHNICAL SUMMARY

Frequency Range	2265 MHz to 2735 MHz
Output Level	+7 dBm, nominal (-2, +3 dB)
Frequency Stability	Options 1 & 3: ±0.00025% Options 2 & 4: ±0.002%
Frequency Deviation Adjustment (approximate)	+3.5 kHz } at crystal -9.0 kHz } frequency
Spurious and Harmonic Rejection	-50 dBm0
Crystal Frequency Range	89.28 MHz to 104.17 MHz
Crystal Frequency	$F_{CH} \pm IF$ where IF = 35 MHz N and N = 24 or 28
TPI (Diode Current)	15 uA ± 5 uA

LAST NO'S USED:

FL1, Y1, L7, Q4, W2, CR1, TPI, R24, C22.

MATERIAL:
FINISH:
PROCESS:
USED ON: LR2-2500
SCALE: _____ ENG. BY: R.J.P. APP.:
DATE: 19/03/85 DWN. BY: L.K. APP.:



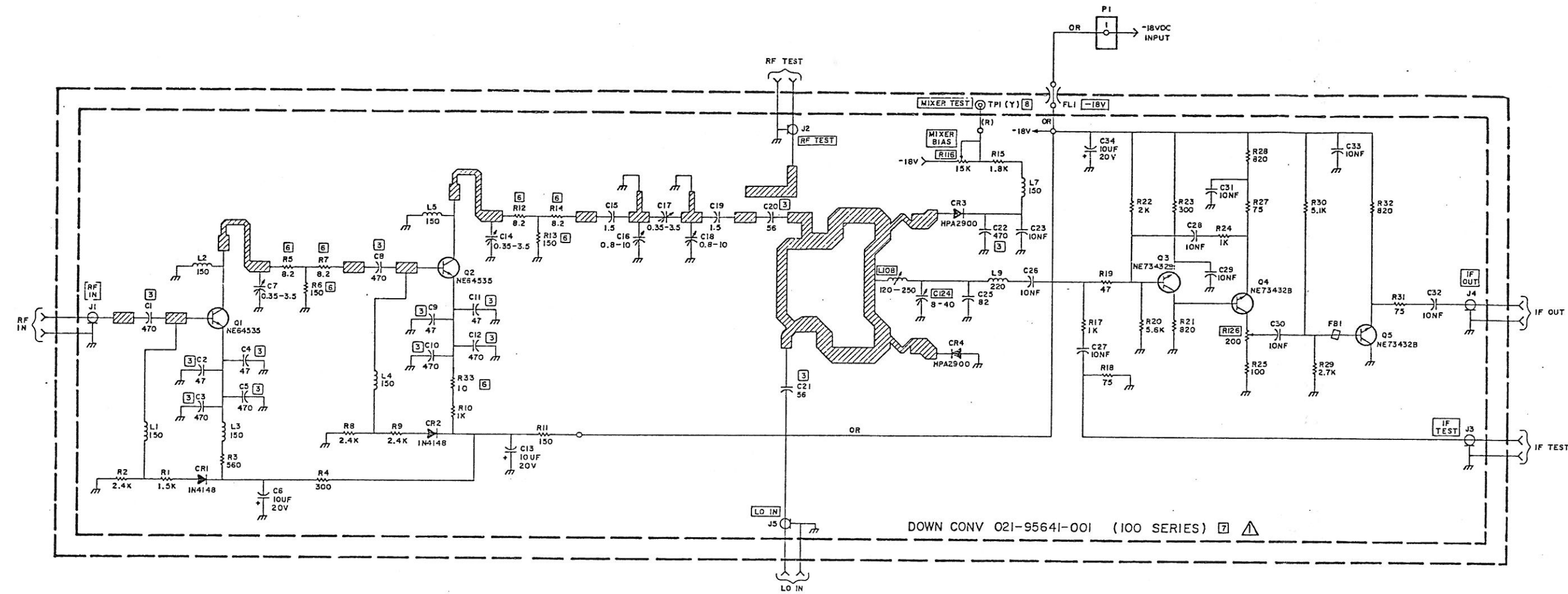
LOCAL OSCILLATOR
2.3 - 2.7 GHz

ISSUE NUMBER CROSS-REFERENCE

SD	1						
TI	1						

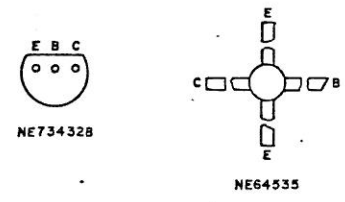
2 DECIMAL TOLERANCE = .015" | SHEET 1 OF 1 | SIZE

REVISIONS		
ISSUE	APP.	DATE
1	B.D.	18-6-82
ADDED NOTE [7], TABLE "A" OPTS. A & B. ECN C4115.		
2	L.J.P.	1-4-85
DELETED L6, 150 OH. R/BY R133. ECN C4328.		
3	R.S.P.	26-11-85
TPI WAS RED. ADDED NOTE [8] ECN C4426		
4	R.S.P.	16-5-86



NOTES:

- UNLESS OTHERWISE SPECIFIED:
 - RESISTOR VALUES ARE IN OHMS \pm 5%, 1/4 W.
 - CAPACITOR VALUES ARE IN PICO FARADS
 - INDUCTOR VALUES ARE IN NANOHENRIES
 - ALL 1% RESISTORS ARE 1/8 W.
- COMPONENT TYPE NUMBERS AND VALUES ARE THOSE NORMALLY PROVIDED. SUBSTITUTIONS OF EQUIVALENT TYPES OR DIFFERING VALUES THAT DO NOT DEGRADE PERFORMANCE MAY BE MADE AT THE TIME OF MANUFACTURE.
- CHIP CAPACITOR.
- SHADED AREAS REPRESENT MICROSTRIP CIRCUITRY WHICH IS PART OF PRINTED CIRCUIT BOARD.
- TRANSISTOR BASE CONFIGURATION (BOTTOM VIEW) SHOWN BELOW.



- [6] - R105 - R107 AND R112 - R114 & R133 ARE CARBON COMPOSITION ONLY.
- [7] - PC BOARD CONFORMAL COATING EQUIPPED PER TABLE "A".
- [8] - FACTORY TEST POINT ONLY.

TABLE "A" [7]

OPT	CONFORMAL COATING
A	OMIT
B	EQUIP

TECHNICAL SUMMARY

Input Characteristics
 Receive Signal (RF) 790 MHz to 960 MHz
 RF Level -40 dBm, nominal
 Local Oscillator Signal (L.O.) 755 MHz to 995 MHz
 L.O. Level +1 dBm to +7 dBm
 Impedance (RF & L.O.) 50 Ohms, unbalanced

Output Characteristics
 Intermediate Frequency (IF) 35 MHz, nominal
 IF Level 0 dBm, nominal
 Conversion Gain 40 dB, typical
 Impedance 75 Ohms, unbalanced
 Return Loss 20 dB, minimum

Noise Figure 3 dB, maximum

TP1 (MIXER TEST) Indication (using a high-impedance voltmeter) -4 Vdc to -7 Vdc, normal

LAST NUMBERS USED
 P1, FL1, TPI
 J105, R132, C134, O105
 CR104, L108, FB101

NUMBERS OMITTED
 L106

ISSUE NUMBER CROSS-REFERENCE

SD	1	2	3	4	5	6	7	8	9	0
TI	1	1	1	1	1	1	1	1	1	1

MATERIAL:
 FINISH:
 PROCESS:
 USED ON: LR2-900, LR4-900
 SCALE: ENG. BY: B.D. APP.:
 DATE: 12/05/82 DWN. BY: N.W. APP.:

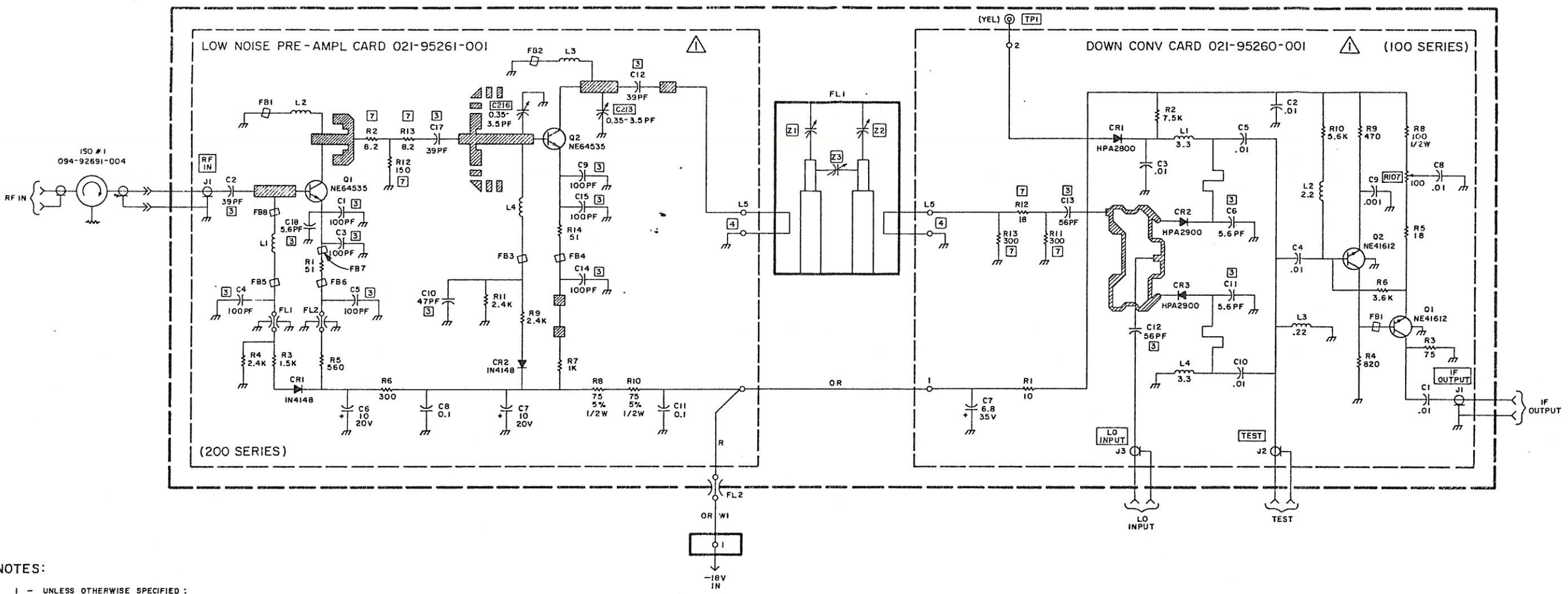
Farinon
Canada

DOWN CONVERTER

2 DECIMAL TOLERANCE \pm .015"
 3 DECIMAL TOLERANCE \pm .005"

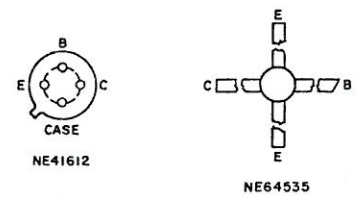
SD-95640 SHEET 1 OF 1 SIZE D

REVISIONS		
ISSUE	APP	DATE
1	S.C.	21-10-82
TPI WAS (GN) . ECN C 3872		
2	R.S.P.	06-02-84
ON PRE-AMPL CARD ADDED FB7, FB8 & C18. ECN C4811.		
3	B.C.	87-10-19



NOTES:

- 1 - UNLESS OTHERWISE SPECIFIED:
 -- RESISTOR VALUES ARE IN OHMS ± 5%, 1/4W.
 -- CAPACITOR VALUES ARE IN MICROFARADS.
 -- INDUCTOR VALUES ARE IN MICROHENRIES.
 -- ALL WIRING IS #22 AWG.
- 2 - COMPONENT TYPE NUMBERS AND VALUES ARE THOSE NORMALLY PROVIDED. SUBSTITUTIONS OF EQUIVALENT TYPES OR DIFFERING VALUES THAT DO NOT DEGRADE PERFORMANCE MAY BE MADE AT THE TIME OF MANUFACTURE.
- 3 - CHIP CAPACITORS.
- 4 - COUPLING LOOP, L105, L205 ARE #22 AWG WIRES, MOUNTED APPROXIMATELY .22 ABOVE THE PRINTED CIRCUIT BOARD
- 5 - SHADED AREAS REPRESENT MICROSTRIP CIRCUITRY WHICH IS PART OF PRINTED CIRCUIT BOARD.
- 6 - TRANSISTOR BASE CONFIGURATION (BOTTOM VIEW) SHOWN BELOW.
- 7 - CARBON COMPOSITION RESISTOR ONLY.



LAST NUMBERS USED
 W1, FL2, TPI, ISO # 1
 Q102, FB101, J103, CR103,
 L105, C113, R113
 J201, Q202, CR202, FL202,
 FB208, C218, L205, R214

ISSUE NUMBER CROSS-REFERENCE

SD	1	2							
TI	1	1							

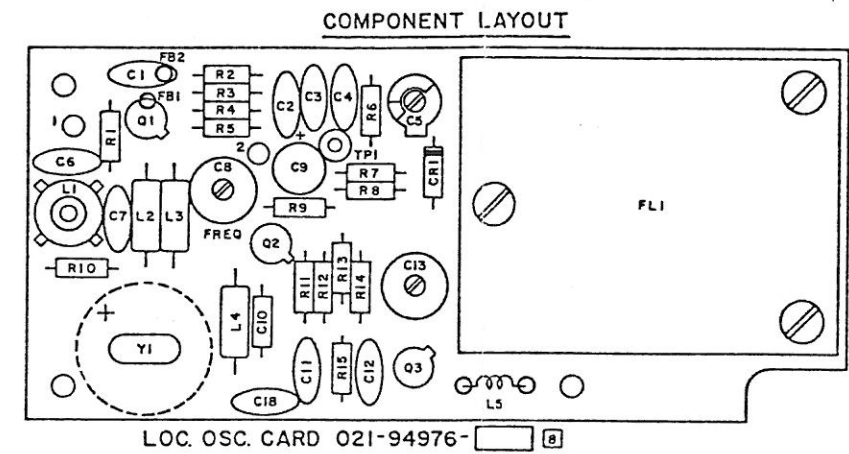
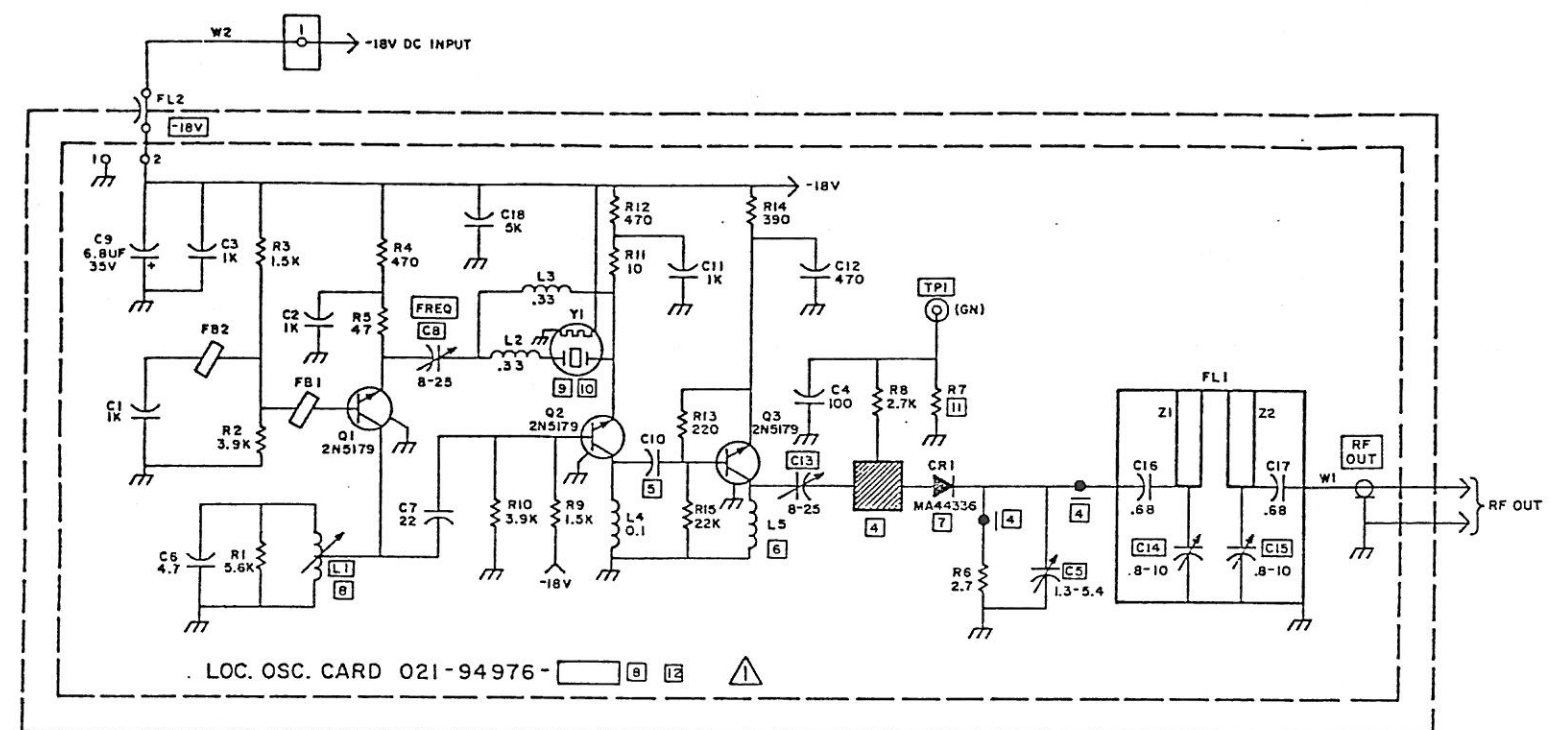
MATERIAL:
 FINISH:
 PROCESS:
 USED ON: LR2-1500
 SCALE: --- ENG. BY: J.L. APP.:
 DATE: 87-10-13 DWN. BY: N.W. APP.:

Farinon
Canada

DOWN CONVERTER

2 DECIMAL TOLERANCE ±.015"
 3 DECIMAL TOLERANCE ±.
 SHEET 1 OF 1 SIZE
 SD-95259 D

SD-94966		
REVISED		
ISSUE	APP.	DATE
1	RUP	20-6-60
ALSO NOTE [13], TABLE "C"		
OPTS. A & B. ECH C4113		
2	R.J.P.	1-4-85



TECHNICAL SUMMARY

Frequency Range	755 MHz to 995 MHz
Frequency Stability	±0.002% ±0.00025%*
Frequency Adjustment Range	±5 kHz, approximately (at crystal frequency)
Output Level	+5 dBm, nominal (±2 dB)
Spurious and Harmonic Rejection	40 dBm, minimum
Output Impedance	50 Ohms, unbalanced
Power Requirements	30 mA maximum, at -18 Vdc or 85 mA maximum, at -18 Vdc*
Connectors	SMB female
Output Power	Multiple plug

* (equipped with optional crystal oven)

TABLE "B" [13]

RCVR FREQ (MHz)	LOC OSC FREQ (MHz)	CRYSTAL FREQ (MHz)	CRYSTAL FORMULA
790 - 850	755 - 815	94.4 - 101.9	$FC - \frac{35}{8}$
	825 - 885	82.5 - 88.5	$FC + \frac{35}{10}$
850 - 960	815 - 925	81.5 - 92.5	$FC - \frac{35}{10}$
	885 - 995	88.5 - 99.5	$FC + \frac{35}{10}$

TABLE "C" [13]

OPT	CONFORMAL COATING
A	OMIT
B	EQUIP

- NOTES:**
- UNLESS OTHERWISE SPECIFIED:
 - RESISTOR VALUES ARE IN OHMS ± 5%, 1/4W
 - CAPACITOR VALUES ARE IN MICROFARADS.
 - INDUCTOR VALUES ARE IN MICROHENRIES.
 - TRANSISTOR DIODE AND INTEGRATED CIRCUIT TYPE NUMBERS SHOWN ARE THOSE NORMALLY EQUIPPED. SUBSTITUTIONS OF EQUIVALENT TYPES OR DIFFERING VALUES THAT DO NOT DEGRADE PERFORMANCE MAY BE MADE AT THE TIME OF MANUFACTURE.
 - TRANSISTOR BASE CONFIGURATION, (BOTTOM VIEW) SHOWN AT RIGHT.
 - PART OF PRINTED CIRCUIT BOARD.
 - C10 IS FACTORY SELECTED; FOR BEST PERFORMANCE SELECT VALUES PER TABLE "A".
 - L5 MAY BE TUNED, (EXPANDED OR CONTRACTED) TO GIVE BEST PERFORMANCE.
 - USE ONLY DIODE TYPE MA44336 FOR CR1. SUBSTITUTION NOT ALLOWED. MOUNT DIODE APPROXIMATELY 1/16" ABOVE BOARD.
 - OPTIONS ARE EQUIPPED PER TABLE "A".
 - CRYSTAL FREQUENCY IS DETERMINED BY FORMULA GIVEN IN TABLE "B".
 - TYPE OF CRYSTAL USED FOR .002% IS 086-R002, FOR .00025% 80°C IS 086-R134 WITH CRYSTAL OVEN 086-20116-20
 - FACTORY SELECTED COMPONENT, NOMINAL VALUE R7, 330Ω.
 - PC BOARD CONFORMAL COATING EQUIPPED PER TABLE "C".

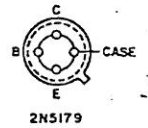


TABLE "A" [8]

OPT	FREQ STABILITY [10]	CRYSTAL FREQ (MHz)	CRYSTAL OVEN	C10 [5]	L1
-001	.002%	81.5 - 85	OMIT	8.2 OR 6.8	071-92994-016
-002		85 - 91.25		6.8 OR 4.7	071-92994-015
-003		91.25 - 101.9		4.7 OR 3.3	071-92994-015
-004	.00025%	81.5 - 85	EQUIP	8.2 OR 6.8	071-92994-016
-005		85 - 91.25		6.8 OR 4.7	071-92994-015
-006		91.25 - 101.9		4.7 OR 3.3	071-92994-015

LAST NUMBERS USED
FL2, Z2, C18, Q3, Y1, L5,
CR1, R15, TPI, FB2, W2

MATERIAL:

FINISH:

PROCESS:

USED ON: SD-100038C LR2-900, LR4-900

SCALE: _____ ENG. BY: R.J.P. APP: _____

DATE: 5/3/80 DWN. BY: H.W. APP: _____

Farinon ELECTRIC

**LOCAL OSCILLATOR
900 MHz**

3 DECIMAL TOLERANCE ±.015"
3 DECIMAL TOLERANCE ±.015"

SHEET 1 OF 1

SD-94966

ISSUE NUMBER CROSS-REFERENCE

SD	1	2							
TI	1	1							